

K HIMANTHA KUMAR

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☐ **CAREER OBJECTIVE:**

Seeking position to utilize my skills and abilities in designing and development of RF, Digital and R&D through investing the best of my qualification, Internship and Work experience.

☐ **WORK EXPERIENCE:**

- ✓ **Organization name** : M/s Cadmax Pvt.Ltd(Client LRDE,DRDO)
- ✓ **Role** : RF Design Engineer
- ✓ **Description** : Involved in various Multilayer and Multifunctional PCB projects for designing the PCB and Power divider. understanding the issues like SI,PI,Thermal analysis. Involved in developing the Schematics from end to end and designing the board file. according to the Schematics placements should be done and took care of doing placements and routing very carefully. Involved in Generating the Gerber file and Creating the Bill of material(BOM).and also Optimized the 1:16 power divider till its gets the best result(Gain and Bandwidth)
- ✓ **Duration** : Sep2023-Jan2024

☐ **INTERNSHIP PROGRAM:**

- ✓ **Organization name** : LRDE-DRDO
- ✓ **Title** : "Antenna array synthesis and analysis for radar applications"
- ✓ **Description** : Main Goal of this Project was to design, optimize and analyze the array of antenna to get the wide scan angle and wide bandwidth for X band Radar application.
- ✓ **Duration** : Aug2022-Aug2023

☐ **PROJECT DETAILS:**

- Title** : "Traffic priority control for ambulance using smart dividers"
- Description** : Main Goal of this project is to provide a system that allows ambulances to move through traffic more quickly and safely, thereby reducing response times and increasing the likelihood of positive health outcomes for patients. The proposed system uses the intelligent transportation systems (ITS) that enable ambulances to communicate with traffic signals to gain priority clearance more efficiently.

☐ **ACADEMIC QUALIFICATION:**

- ✓ **Bachelor of Technology (B. TECH)** in Electrical and Electronics Engineering from **M.S Ramaiah university of applied sciences, Bangalore**
- ✓ Current CGPA (till 8th semester: 8.01)
- ✓ **Graduated : 2023**
- ✓ **Diploma** from **S.V. Govt, polytechnic college, Tirupati**, Andhra Pradesh in the Year 2020 with 78.32%
- ✓ **SSC** from Sudhaha little citizens high school, Tirupati, Andhra Pradesh in the Year 2016 with 88%

☐ **SOFTWARE SKILLS:**

- ✓ Cadence Allegro
- ✓ PCB Design (Cadence Allegro)
- ✓ ADS (Advanced Design System)
- ✓ LT spice
- ✓ ORCAD capture
- ✓ CST studio suite
- ✓ Autodesk Eagle
- ✓ Fusion 360
- ✓ Basics of VHDL (programming language)
- ✓ MATLAB
- ✓ C language

☐ **PERSONALSKILLS:**

- ✓ Willingness to learn new technologies and take responsibility.
- ✓ Hardworking and have a lot of patience and flexible.
- ✓ Ability to work individually as well as in a group environment.

☐ **TECHNICALSKILLS:**

- ✓ Multilayer multi-functional PCB design.
- ✓ Antenna design and optimization.
- ✓ RF hardware design.

☐ **PERSONALDETAILS:**

Father's Name : K Durga prasad
Date of Birth : 20/07/2001
Sex : Male
Marital Status : Single
Languages Known : Kannada, English, Telugu, and Hindi.
Present Address : #9, Sai nilayam, 2nd A cross road, 5th main,
Munnekollal, Marathahalli, Bengaluru.
Permanent Address : 16-1-37, Rc/2, Karnala street, Tirupati, Andhra
pradesh-517501

□ **REFERENCE:**

Dr. Veer Singh Gangwar Scientist-F,
LRDE (DRDO)
C V Raman Nagar,
Bangalore
Email: [_](#)

DECLARATION:

I hereby declare that the above mentioned particulars are true and correct to the best of my knowledge and belief.

(K HIMANTHA KUMAR)

