



### CAREER OBJECTIVE

To work in a firm with a professional work driven environment where I can utilize and apply my knowledge, skills which would enable me as a fresh graduate to grow while fulfilling organizational goals.

### Skills Summary

**B.E.(Electronics) from KJSIET,Sion under Mumbai University**

CGPA	Semester 8	Semester 7	Semester 6	Semester 5	Semester 4	Semester 3
6.51	7.79	7.11	6.11	7.11	5.57	5.42

### Pre Engineering:

Qualification	Institute	Board/University	Year	%
Dimploma(Electronics& Telecommunication)	G.V. Acharya	Maharashtra State	2014	57.55
10 <sup>th</sup>	St John The Baptist High School.	Maharashtra State Board.	2011	70.18

### Professional Certifications/ Trainings

- Certified System Administrator RadHat Linux 7(RHEL7) from RST Forum, Matunga since Feb'2017
- Smart Robotics Workshop Level II, where I made a Wireless controlled Robot used for surveillance.
- State Level Project Exhibition where I made voice communication project using LASER.
- Participated in State Level Quiz Competition organized by ETSSA.
- Persuing CEH certificate form Ec-council.
- persuing CPTe course from pristine.

### Projects Undertaken

#### Project 1

<b>Project Name</b>	<b>Artificial Solar Oxygen Tree.</b>	<b>Team Size</b>	3
<b>Start Date</b>	Sep 2017	<b>End Date</b>	Mar 2018
<b>Project Description</b>	<p>This project was executed with the objective of harnessing revealable energy source(sunlight). In here we used solar panel to charge battery which would supply a set amount of voltage to microcontrol circuit that</p> <ul style="list-style-type: none"> <li>• Controls the display items for LCD.</li> <li>• Controls streat light using LDR.</li> </ul>		

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	<ul style="list-style-type: none"><li>Performs electrolysis on waste water and release Hydrogen and Oxygen in environment.</li></ul>
<b>Technology &amp; Tools</b>	MC 89C51, Solar Panel, KIEL and IC universal burner kit.

<b>Project 2</b>			
<b>Project Name</b>	Logic gate using 8051	<b>Team Size</b>	1
<b>Start Date</b>	Sep 2016	<b>End Date</b>	Dec 2016
<b>Project Description</b>	Project was designed with the objective of demonstrating all logical truth tables. In here I used microcontrol 89C51 along with toggle switch board and LED indicator for each of logical gate. Depending on the switch selection an appropriate LED indicator would display success of a logical gate selection.		

#### **Personal Details.**

Date of Birth 17<sup>th</sup> Sep 1995  
Languages Known Gujrati, English, Hindi  
Address 1st Janki Niwas Opposite Civil Hospital Thembhi Naka, Thane (W)  
400601  
Hobbies Swimming, Playing Badminton, Listening To Music.

**Interest:** Information security in VAPT

**Skills:** Vulnerability Assessment of Servers ,Configuration Assessment of Window

Servers 2008,2012/2016, RHEL7/6, Oracle 2016/2017,SQL 2016/2017.

**Tools:** Nessus SC,Nmap,Metasploit,Burp suite.



Experience:	
Security Analyst	<p>Company:Inspira Enterprise Indian Private Limited</p> <p>Joining Date: 6 july 2020 Last Date:2 Feb 2021</p> <p>Previous Company: Talakunchi Networks pvt ltd till 2019-2020 Joining: 5 march 2019. last Date: 28 Jun 2020</p> <p>Client: Bank</p> <p>Experience:1.10 years</p> <p>Skills: My role is configuration assessment of servers(CA) and vulnerability assessment (VA)</p> <p>In configuration management:  Conducting CIS benchmarks policy for OS system with help of nessus tool. Eg window 12,16,19, rhel6,7,8. Editing nessus audit script file as per the organization. Resolving false positive issues. Creating dashboard for management and rbi audit.</p> <p>In vulnerability Management: Scanning server and provide report to IT teams. Resolving false positive issue. Dashboard for management and Audit</p> <p>Tool:Nessus,nexpose,itgrc</p>

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**Declaration:**

**I hereby declare that the above given information is true to the best of my knowledge and belief.**

**Date:**

**Place: Thane**

**Signature**