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Patent Engineer with more than 5 years of experience in patent searching, market analysis, and more than 1.5 years of experience in patent prosecution and patent drafting. I am a Post Graduate in Microelectronics and Embedded Technology with experience in ADS and other CAD tools. I also have experience working on standard essential patents (SEP) and portfolio management.

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### **SKILLS, STRENGTHS & ACHIEVEMENTS**

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- Worked on Patent Prosecution responses for US, EP, CN and other major jurisdictions.
- Worked on Patent drafting of US patent applications.
- Managing a portfolio independently
- Conducted several interviews with inventors for patent drafting and patent analysis, product clearance
- Worked on Patentability searches, Invalidity searches, Landscape searches, Freedom-to-Operate searches, State-of-the-Art searches, Infringement searches, Market Research, Portfolio Analysis. Also have experience in studying Prosecution History/ Image File wrapper Analysis.
- Closely worked with US attorneys on drafting, prosecution, and patent analytics matters.
- Legal knowledge of Patent Filing, Patent Prosecution and Responding to Office Actions under US Patent Office.
- Used Patent and Non-Patent Literature databases such as Innography, Thomson Innovation, Orbit, LexisNexis Patent Strategies, ETSI, 3GPP, Google Patents, Patentlens, KIPRIS, JPAT, Espacenet, Patseer, Google Scholar, Science Direct

### **RELEVANT WORK EXPERIENCE**

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#### **RESEARCH ASSOCIATE LEVEL 3 – EFFECTUAL KNOWLEDGE SERVICES, NOIDA (ELECTRONICS & COMMUNICATION)**

**JAN 2016 – MAY 2019**

##### *Responsibilities included:*

- Performing novelty, patentability and prior art searches and collating results into reports.
- Carrying out projects such as Patentability searches, Invalidity searches, Landscape searches, Freedom-to-Operate searches, State-of-the-Art searches, Market Research, Portfolio Analysis, Standard Mapping, Infringement searches, across all Electronics and Communication domain.
- Training Analysts, performing quality check and providing feedback, estimating effort and delivering timely reports.
- Collating results into an MS Excel, MS Word or MS PowerPoint report.
- Reporting the stage of Prosecution History in clearance studies with an update on scope of claimed rights.

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#### **IP ANALYST II - HONEYWELL TECHNOLOGY SOLUTIONS LAB, BENGALURU**

**MAY 2019– PRESENT**

##### *Responsibilities include:*

- Portfolio maintenance.
- Preparing responses for patent prosecution mainly in US, EP, and CN jurisdictions.
- Drafting patent applications mainly in US jurisdiction.
- Connecting with engineers to perform IP clearances on products to be launched.
- Working closely with US attorneys for patent prosecution and analytics matters.
- Recommending design around ideas to engineers.
- Performing novelty, patentability and prior art searches and collating results into reports.
- Carrying out projects such as Patentability searches, Invalidity searches, Landscape searches, Freedom-to-Operate searches, State-of-the-Art searches, Market Research, Portfolio Analysis, Standard Mapping, Infringement searches.

<b>EDUCATIONAL QUALIFICATION</b>		
M.TECH (MICROELECTRONICS AND EMBEDDED TECHNOLOGY)	JAYPEE INSTITUTE OF INFORMATION TECHNOLOGY, NOIDA	GPA: 7.8 July 2016
B. TECH (ELECTRONICS AND TELECOMMUNICATION)	MBES COLLEGE OF ENGINEERING, AMBAJOGAI, MAHARASHTRA	69.21% May 2013