



VIJAYANAND MURUGESAN



SI-2 POLICE QUARTERS
SBI BACK SIDE
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About Me

PROFESSIONAL ENGINEER

Seeking for a Challenging Position as a Professional, where in given chance of providing my Skills and Ability in the Information Technology field that offers Professional growth while being Resourceful, Innovative and Flexible. To accomplish my responsibilities to the best of my Efficiency and to Work hard to achieve Organizational objectives.

Education

10th

SRI VIJAY VIDYALAYA B. MHSS
MATRIC
DHARMAPURI, Tamil Nadu

Completed, May 2009
Marks 79%
Division I

HSC (12th), MATHS-COMPUTER SCIENCE

SRI VIJAY VIDYALAYA G.MHSS
STATE BOARD
DHARMAPURI, Tamil Nadu

Completed, May 2011
Marks 86%
Division I

Bachelor of Engineering / Bachelor of Technology, ELECTRONICS AND COMMUNICATION ENGINEERING

VELAMMAL ENGINEERING COLLEGE
ANNA UNIVERSITY CHENNAI
CHENNAI, Tamil Nadu

Completed, July 2015
Marks 61%
Division II

Master of Business Administration, HUMAN RESOURCE MANAGEMENT

PERIYAR UNIVERSITY
PERIYAR UNIVERSITY
SALEM, Tamil Nadu

Completed, July 2017
Marks 63%
Division I

Master of Social worker.

PERIYAR UNIVERSITY
PERIYAR UNIVERSITY
SALEM, Tamil Nadu

Completed, July 2019
Marks 67%
Division I

Awards & Honors

- Awarded 3rd price in "ELEKTRONICA'13 - A INTRA PROJECT EXPO" held in "VELAMMAL ENGINEERING COLLEGE".
- Achieved council of Europe Level A2 in "UNIVERSITY of CAMBRIDGE ESOL EXAMINATION". (Ref No : 129IN0010128).

Skills

- Adaptability.
- Creative and Innovative thinking.
- Team facilitator.
- Quick learner.
- Good time management and Hard-Worker.

Computer Proficiency

Operating Systems

- UBUNTU ●●●●
- WINDOWS 7 ●●●●●
- WINDOWS 8 ●●●●●

Software

- C, C++ ●●●●
- M.S OFFICE ●●●●●

Languages

- Tamil ●●●●●
- English ●●●●●

Professional Affiliations

- Member of IETE Association.
- UTHIRAM BLOOD INFO BANK Member.

Personal Interests

- Writing Tamil Poems.
- Out-Door Sports.

Extra Curricular Activities

- Long-Term Dedication
- Adaptability and flexibility
- Comprehensive problem Solving Skills
- Quick Leaner
- Good Time Management and Hardworker.

Projects

Project 1

DESIGN AND IMPROVING "PCO BASED SNAPPER"

Period:

August 2013 - September 2013

Project Summary

The Study of the Project is about to determine the "Avoiding The FRANK CALLS and BOMB THREATS".

Project 2

AN IMPLEMENTATION OF FPGA BASED RECONFIGURABLE HARDWARE ARCHITECTURE FOR POLYNOMIAL MATRIX MULTIPLICATION

Period:

February 2015 - April 2015

Project Summary

To reduce time for the matrix multiplication and And to implement the reconfiguration in the polynomial Matrix multiplication Using VLSI TECHNOLOGY.

Project 3

A STUDY ON SOLID WASTE MANAGEMENT & ENVIRONMENT
POLLUTION REVIEW ARTICLE

Period: April 2019 - May 2019

Project Summary

An attempt to reduce the MATERIAL SOLID WASTE by implementing modern techniques to decentralized solid waste in metro-politan cities in the way of creating awareness.

Project 4

REVIEW ARTICLE ON ENVIRONMENTAL CHEMISTRY AND BIOLOGICAL
POLLUTION

Period: April 2018 - May 2018

Project Summary

An awareness on pollution that are been increased due to Urbanization based development on factors like increasing chemical factories and the outlet waste from those factories.

Personal Details

Father's Name: Mr. MURUGESAN N
Birthday: July 30, 1994
Gender: Male

Mother's name: Mrs.KANNAMMAL C
Marital Status: Single
Nationality: India

Declaration

I, VIJAYANAND MURUGESAN, hereby declare that the information contained herein is true and correct to the best of my knowledge and belief.

VIJAYANAND MURUGESAN

DHARMAPURI , Tamil Nadu

